

**Date:** <u>2006.05.12</u>

# **Specifications for Approval**

Cus	tomer <u>:</u>						
Model name : LMC2023A-YTDDON-A0 REV:A							
Description : LIQUID CRYSRAL DISPLAY MODULE							
•							
	DESIGN		CHECK	APPROVED			
	Customer Approval		Accept Reject Comment:	oved by:			



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#### LCM MODULE NUMBERING SYSTEM

PART NUMBER: LMAx.....yB-CDEFGHI-JK

L: LONDA TECHNOLOGY

M: MODULE

A: DISPLAY CONTENTS C--- CHARACTER TYPE

G--- GRAPHIC TYPE S---SEGMENT TYPE

x.....: CHARACTERS Vs. LINES FOR CM

COLUMNS Vs. ROWS FOR GM SERIALS NUMBER FOR SM

y: DISTRIBUTE ACCORDING TO SIZE

**B**: VERSION OF PCB

C: LCD TYPE:

Y---YELLOW STN G---GRAY STN B---BLUE STN

F---FSTN T---TN

D: POLARIZER TYPE:

R--- REFLECTIVE T--- TRANSFLECTIVE

M--- TRANSMISSIVE

**E: VIEWING TEMPRETURE:** 

U--- 12:00 D--- 6:00 L--- 9:00 R--- 3:00

F: BACKLIGHT TYPE:

D---BOTTOM LED S---SIDE LED E--- EL C--- CCFL

N---NO BACKLIGHT

**G: COLOR OF BACKLIGHT:** 

Y---YELLOW/GREEN G--- GREEN B--- BLUE

W--- WHITE O--- ORANGE A--- AMBER

**H:OPERATING TEMPRETURE:** 

N--- NORMAL, W--- EXTENDED, X:ESPECIALLY EXTENDED

I: DENOTE DIFFERENT CHARACTER TABLE:

NORMAL ELLIPSIS, T--- TAB, G--- COG

-JK:

FOR CM:

J: IC TYPE: A--- KS0066U B--- SPLC780

K: CHARACTER STOREROOM SEQUENCE NUMBER

FOR GM:

J: BACKLIGHT DRIVER

Y--- WITH N--- WITHOUT

**K: DC-DC CONVERTER** 

Y--- WITH N--- WITHOUT

#### 1. FEATURES

The features of LCD are as follows

\* Display mode : STN, Positive, Transflective

\* Color : Display dot : Dark Blue

Background: Yellow-Green

\* Display Format : 20 X 2 Characters

\* IC : S6A0069 and S6A0065

\* Interface Input Data : 4-bit or 8-bit Parallel

\* Driving Method : 1/16 Duty, 1/5 Bias

\* Viewing Direction : 6 O'clock

\* Backlight : LED (Orange)

#### 2. MECHANICAL SPECIFICATIONS

Item	Specification	Unit
Module Size	180(W) x 40(H) X 14.5MAX(T)	mm
Viewing Area	149(W) X 23(H)	mm
Effective Display Area	143.88(W)X 20.40(H)	mm
Character Font	5 X 8 with Cursor	
Character Pitch	7.26(W) X 10.86(H)	mm
Character Size	5.94(W) X 9.54(H)	mm
Dot Pitch	1.20(W) X 1.20(H)	mm
Dot Size	1.14(W) X 1.14(H)	mm

#### 3. ELECTRICAL SPECIFICATIONS

#### 3-1. Absolute Maximum Ratings (Vss=0V)

ltem	Symbol	Sta			
item	Syllibol	Min.	Тур.	Max.	Unit
Supply Voltage For Logic	VDD-Vss	-0	-	+5.5	V
Supply Voltage For LCD Drive	VDD-V0	-0	-	+5.5	V
Input Voltage	Vin	Vss	-	Vdd	V
Operating Temp.	Тор	0	-	50	°C
Storage Temp.	Tst	-20	-	70	°C

Notes: Voltages VDD \( \subseteq V1 \( \subseteq V2 \( \subseteq V3 \( \subseteq V4 \( \subseteq V5 \( \subseteq V\_0 \) must always be satisfied.

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# 3. ELECTRICAL SPECIFICATIONS (Continued)

# 3-2. Electrical Characteristics (Vss=0V)

Item		Symbol	Test Condition		Тур.	Max.	Unit
Logic Supply Voltage		VDD – VSS	Ta=0~50°C	4.5	5.0	5.5	٧
LCD Drive Voltage (Recommended Voltage)		VDD - V0	Ta=25°C	-	4.5	-	V
	"H" Level	V <sub>IH</sub>	V <sub>DD</sub> =5.0V ± 5%	0.8	-	VDD	V
Input Voltage	"L" Level	V <sub>IL</sub>	$0.00-3.00\pm 5\%$	0	-	0.2	V
	"H" Level	V <sub>OH</sub>	\/F 0\/ : F0/	0.8	-	VDD	V
Output Voltage	"L" Level	V <sub>OL</sub>	VDD=5.0V ± 5%	0	-	0.2	V
Current Consumption		I <sub>DD</sub>	V <sub>DD</sub> =5.0V ± 5% V <sub>DD</sub> -V <sub>0</sub> =4.5V	6 -	1.34	3.0	mA

NOTE: 1) Duty Ratio=1/16, Bias Ratio=1/5

2) Measuring in Dots ON-state

#### 3-3. BACKLIGHT

# 3-3-1. Absolute Maximum Ratings

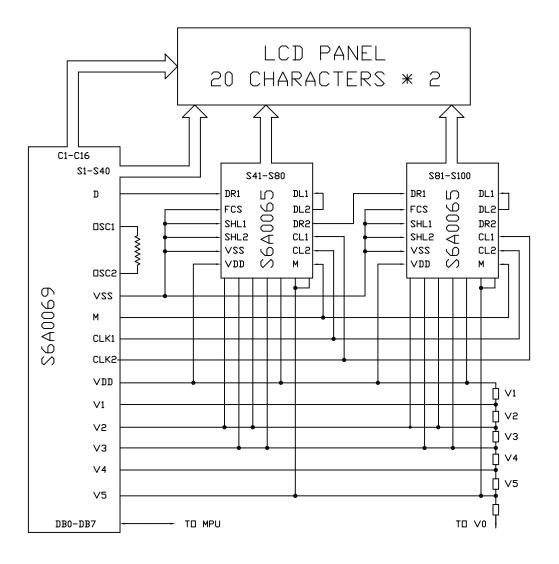
Item	Symbol	Condition	Min.	Тур.	Max	Unit
Forward Current	IF	- Ta= 25°ℂ	-	-	420	mA
Reverse Voltage	VR	1a- 25 C	-	-	8.0	V
Power Dissipation	PD	Ta= 25°℃	ı	ı	1800	mW

# 3-3-2. Opto-electronic Characteristics

Item	Symbol	Condition	Min.	Тур.	Max	Unit
Forward Voltage	VF	Ta= 25°ℂ	-	5.0	5.2	V
Luminous	-	IF= 420mA	-	-	-	cd/m²

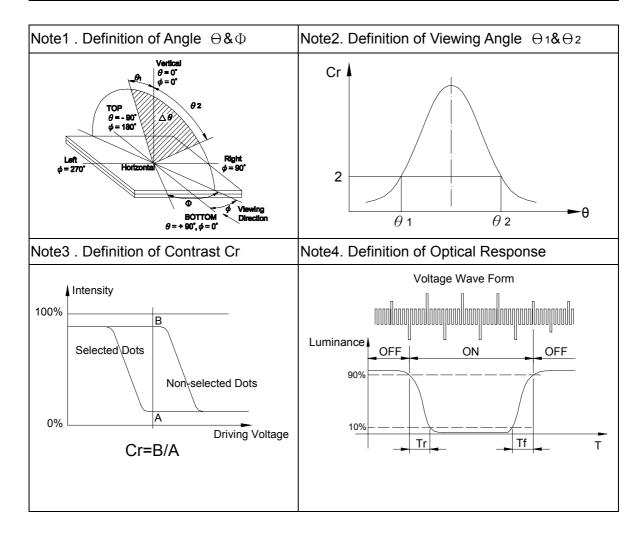
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#### 4. BLOCK DIAGRAM AND POWER SUPPLY



#### 5. ELECTRO - OPTICAL CHARACTERISTICS

Item	Symbol	Temp.	Min.	Тур.	Max.	Unit	Conditions	Note
Viewing	⊖2-⊖1	<b>25</b> ℃	30	92	-	Dog		1.0
Angle	Φ	250	80	90	-	Deg.	-	1,2
Contrast Ratio	Cr	25℃	2	11.1	11.9	-	⊕=0° ⊖=0°	3
Response	Tr	<b>25</b> ℃	-	92	250	ms	Ф=0° ⊖=0°	4
Time(rise)		0℃	-	950	1150			4
Response	Tf	<b>25</b> ℃	-	119	250	m. 0	⊖=0°	4
Time(fall)		0℃	-	950	1150	ms	Ф=0°	4



#### **6. TERMINAL PIN FUNCTION**

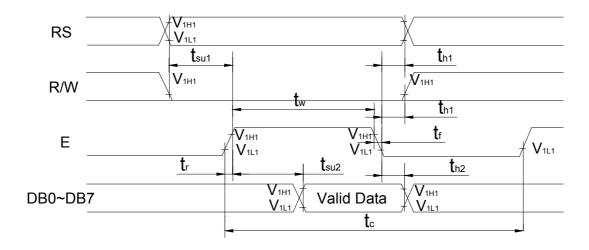
# 6-1. Interface Pin Function Description

Pin NO.	Symbol	1/0	Functions
1	Vss	Power	GND
2	Vdd	Power	Power supply for logic circuit 5.0V
3	V0	Power	Contrast adjustment
4	RS	I	Register select signal
5	R/W	I	Used as read/write selection input when Rw="high" read operation Rw="Low", write operation
6	E	I	Enable signal
7	DB0		
8	DB1		
9	DB2		
10	DB3	I/O	Data bus
11	DB4	1/0	Data bus
12	DB5		
13	DB6		
14	DB7		
15	LED(A)	-	Backlight(+) 5.0V
16	LED(K)	-	Backlight(-)

#### 7.TIMING CHARACTERISTICS

7-1. Write Mode (Writing data from MPU to LCM)

Mode	Symbol	Min.	Тур.	Max.	Unit
E Cycle Time	tc	500	-	-	ns
E Rise / Fall Time	tr, tr	-	-	20	ns
E Pulse Width (High, Low)	tw	230	-	-	ns
R/W and RS Setup Time	<b>t</b> su1	40	-	-	ns
R/W and RS Hold Time	t <sub>H1</sub>	10	-	-	ns
Data Setup Time	tsu2	80	-	-	ns
Data Hold Time	t <sub>H1</sub>	10	-	-	ns

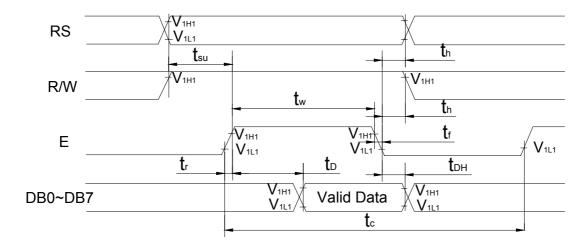


Write Mode Timing Diagram

# 7.TIMING CHARACTERISTICS (Continued)

# 7-2. Read Mode (Reading data from MPU to LCM)

Mode	Symbol	Min.	Тур.	Max.	Unit
E Cycle Time	tc	500	-	-	ns
E Rise / Fall Time	tr, tr	-	-	20	ns
E Pulse Width (High, Low)	tvv	230	-	-	ns
R/W and RS Setup Time	<b>t</b> su1	40	-	-	ns
R/W and RS Hold Time	t <sub>H1</sub>	10	-	-	ns
Data Output Delay Time	tsu2		-	120	ns
Data Hold Time	t <sub>H1</sub>	5	-	-	ns



Read Mode Timing Diagram

#### **8.INSTRUCTION SET**

#### 8-1. Instruction Table

Function	RS	R/ W	DB 7	DB 6	DB 5	DB 4	DB 3	DB 2	DB 1	DB 0	Description	Execu. Time*(Max.)
Clear Display	0	0	0	0	0	0	0	0	0	1	Clear Entire Display	1.53mS
Return Home	0	0	0	0	0	0	0	0	1	*	Return Display Being Shifted to Original Position	1.53mS
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Set Cursor Move Direction And Specifies Shift of Display	39uS
Display ON/OFF Control	0	0	0	0	0	0	1	D	С	В	D: Display On /Off C: Cursor On/Off B: Cursor Blink/Not	39uS
Cursor or Display shift	0	0	0	0	0	1	S/C	R/L	*	*	Move Cursor And Shift Display	39uS
Function Set	0	0	0	0	1	DL	N	F	*	*	Set DL, N, F	39uS
Set CG RAM Address	0	0	0	1			AC	CG			Set CG RAM Address	39uS
Set DD RAM Address	0	0	1				ADD	)			Set DD RAM Address	39uS
Read Busy Flag & Address	0	1	BF				AC				BF: Busy Flag Read AC Contents	0uS
Write Data to CG RAM	1	0			W	RITE	E DA	TA			Write Data to DD RAM or CG RAM	43uS
Read Data From CG/DD RAM	1	1			RE	AD	DA	λTΑ			Read Data From DD RAM or CG RAM	43uS
Remark	I/D S S/O R/I DL N F BF	= C = ' L = : = : = : = : = : = : = : = : = : =	1 : D 1 : \$ 1 : 8 1 : 1 : 5 1 : lı	ccor ispla Shift Bits 2 L x 10	mpai righ ine Donally	nies nift t ts (	0:	olay S Curse Shi 4 Bit 1 Lir x 7	Shift or M ft lef ts ne Dots	ove t	DD RAM : Display Data F CG RAM : Character Ger ACG : CG RAM Addr ADD : DD RAM Add Corresponds to Curs AC : Address Count Both DD and CG RA * No effect (Don't care)	nerator RAM ess ress sor Address er used for

NOTE: When an MPU program with checking the Busy Flag(DB7) is made, it must be necessary 1/2Fosc is necessary for executing the next instruction by the falling edge of the "E" signal after the Busy Flag(DB7) goes to "low".

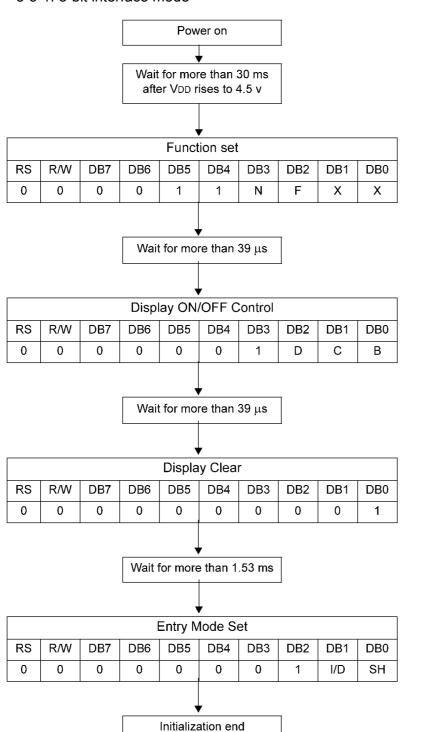
#### 8-2. DDRAM Address

Display position	Column	1	2	3	4	5	I	16	17	18	19	20
DD RAM Address	1-Line	00H	01H	02H	03H	04H		0FH	10H	11H	12H	13H
(Hex-Decimal)	2-Line	40H	41H	42H	43H	44H	i	4FH	50H	51H	52H	53H

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- 8-3. Instruction Initialization
- 8-3-1. 8-bit interface mode



N	0	1-line mode
	1	2-line mode

F	0	display off
•	1	display on

D	0	display off
	1	display on

C	0	cursor off
	1	cursor on

В	0	blink off
	1	blink on

I/D	0	decrement mode
""	1	increment mode

SH	0	entire shift off
011	1	entire shift on

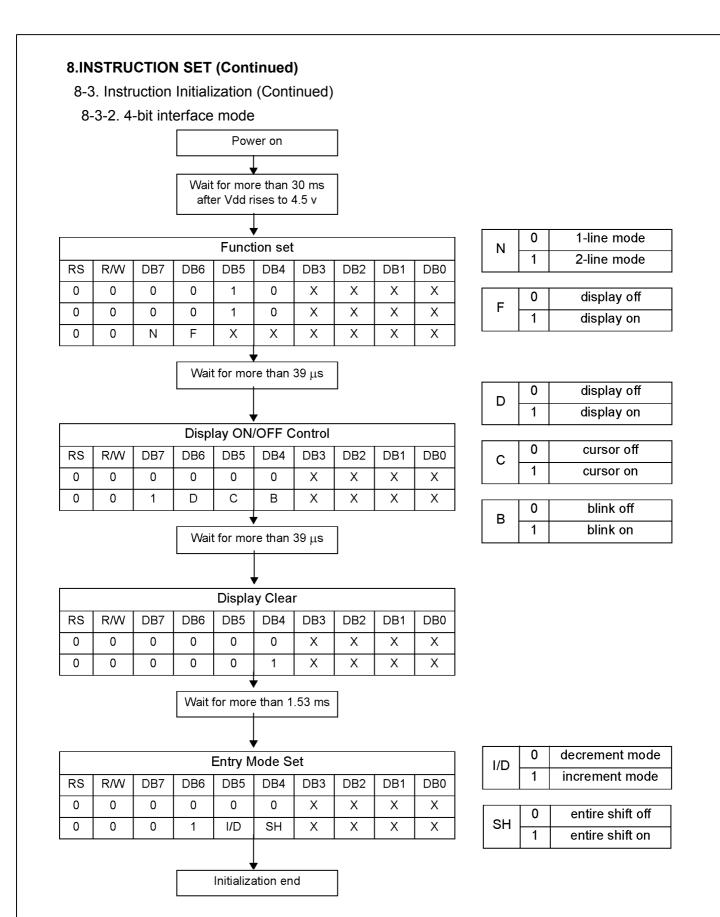
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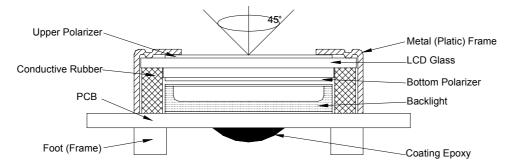


Upper 4 bit Lower 4 bit	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	НГНН	HHLL	ННГН	HHHL	нннн
LLLL															
LLLH															
LLHL															
ГГНН															
LHLL															
LHLH															
LHHL															
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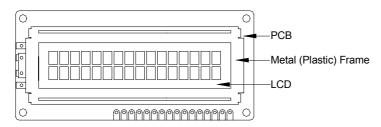
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#### **10. QUALITY SPECIFICATIONS**

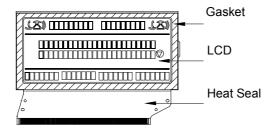
- 10 1. LCM Appearance and Electric inspection Condition
  - 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



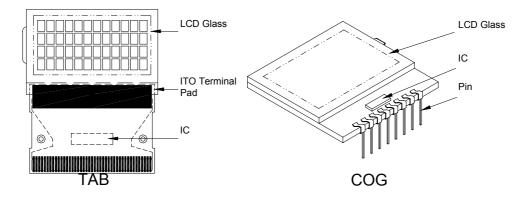
- 2. View Angle: with in 45° around perpendicular line.
- 10 2. Definition
  - 1. COB



2. Heat Seal



3. TAB and COG



#### 10. QUALITY SPECIFICATIONS (Continued)

10-3. Sampling Plan and Acceptance

1. Sampling Plan

MIL - STD - 105E (  $\parallel$  ) ordinary single inspection is used.

2. Acceptance

Major defect: AQL = 0.25%Minor defect: AQL = 0.65%

10-4. Criteria

#### 1.COB

Defect	Inspection Item	Inspection Standards	
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm <sup>2</sup>	Reject
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject
Major	PCB cutting defect	Exceed the dimension of drawing	Reject

#### 2.SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing, extra, wrong component or wrong orientation		Reject
Minor	Component position shift  component soldering pad  D  Z	X < 3/4Z Y > 1/3D	Reject Reject
Minor	Component tilt component soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component PAD	<i>θ</i> ≤ 20°	Reject

# 10. QUALITY SECIFICATIONS (Continued)

- 10-4. Criteria (Continued)
  - 3. Metal (Plastic) Frame

Defect	Inspection Item	lı	nspection Standar	rds		
Major	Crack / breakage	Any	where	Reject		
		W	L	Acceptable of Scratch		
		w<0.1mm	Any	Ignore		
		0.1 <u>&lt;</u> w<0.2mm	L <u>&lt;</u> 5.0mm	2		
Minor	Frame Scratch	0.2 <u>&lt;</u> w<0.3mm	L <u>&lt;</u> 3.0mm	1		
- 		w <u>&gt;</u> 0.3mm	Any	0		
		Note: 1. Above criteria applicable to scratch lines with distance greater than 5mm.  2. Scratch on the back side of frame (not visible) can be ignored.				
				Acceptable of Dents / Pricks		
		Φ<	1.0mm	2		
	Frame Dent , Prick	1.0< ₫	) <u>&lt;</u> 1.5mm	1		
Minor	$\Phi = \frac{L + W}{2}$	1.5r	nm<⊕	0		
	2	/ pricks with dis	criteria applicable tance greater than prick on the back ignored	5mm		
Minor	Frame Deformation	Excee	Exceed the dimension of drawing			
Minor	Metal Frame Oxidation		Any rust			

# 4. Flexible Film Connector (FFC)

Defect	Inspection Item	Inspection Standards				
Minor	Tilted soldering	Within the angle +5°	Acceptable			
Minor	Uneven solder joint /bump		Reject			
Minor	Hole $\Phi = \frac{L + W}{2}$	Expose the conductive line	Reject			
IVIII IOI	2	Φ > 1.0mm	Reject			
Minor	Position shift	Y > 1/3D	Reject			
IVIII IOI	- <del>*</del> - <del>*</del> -	X > 1/2Z	Reject			

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# 10. QUALITY SPECIFICATIONS (Continued)

10-4. Criteria (Continued)

#### 5. Screw

Defect	Inspection Item	Inspection Standards			
Major	Screw missing/loosen		Reject		
Minor	Screw oxidation	Any rust	Reject		
Minor	Screw deformation	Difficult to accept screw driver	Reject		

# 6. Heatseal . TCP . FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $\Phi = \frac{L + W}{2}$	Ф> 0.5mm	Reject
Major	Adhesion strength	Less than the specification	Reject
Minan	Position shift	Y > 1/3D	Reject
Minor	Λ <del>                                     </del>	X > 1/2Z	Reject
Major	Conductive line break		Reject

# 7. LED Backing Protective Film and Others

Defect	Inspection Item	Inspection Standards				
		Acceptable number of units				
		⊕ <u>&lt;</u> 0.10mm	Ignore			
		0.10<⊕ <u>&lt;</u> 0.15mm	2			
Minor	LED dirty, prick	0.15<⊕ <u>&lt;</u> 0.2mm	1			
		Ф>0.2mm	0			
		The distance between any two spots should be > Any spot/dot/void outside of viewing area is acce				
Minor	Protective film tilt	Not fully cover LCD	Reject			
Major	COG coating	Not fully cover ITO circuit	Reject			

# 8. Electric Inspection

Defect	Inspection Item	Inspection Standards	
Major	Short		Reject
Major	Open		Reject

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			1 110 5 0 0 1 0 1 20 11 10 / 11 10 11 0	

# 10. QUALITY SPECIFICATIONS (Continued)

- 10-4. Criteria (Continued)
- 9. Inspection Specification of LCD

Defect	Insp	ect Item			Ins	pection	S	tandard	s	
		* Glass Scratch	W			0.03	0.0	0.03 <w<u>&lt;0.05 V</w<u>		V>0.05
		* Polarizer Scratch	L	L<5			L<3		Any	
Minor	Linear Defect	* Fiber and Linear	ACC. NO.			1		1		Reject
		material	Note	L is th	e ler	ngth and V	V is th	e width of	the de	efect
		* Foreign material	Φ	Φ≤		0.1<Ф <u>&lt;</u> 0	).15 (	0.15<⊕ <u>&lt;</u> 0	.2	⊕>0.2
	Black Spot and	between glass and polarizer or glass	ACC. NO.	3E/ 100n	A / nm²	2		1		0
Minor	Polarizer Pricked	and glass  * Polarizer hole or protuberance by external force	Note					er of the de ects > 10r		
		* Unobvious	Φ		Φ <u>&lt;</u>	0.3	0.3	<⊕ <u>&lt;</u> 0.5	0.	5<⊕
	White Spot	transparant foreign material between	ACC. NO.	3E/	4/10	00mm <sup>2</sup>		1		0
Minor	and Bubble in polarizer	glass and glass or glass and polarizer * Air protuberance between polarizer and glass	Note			ge diameter of the defect. en two defects > 10mm.				
			Φ	Φ <u>&lt;</u> 0.10 0.10<Φ <u>&lt;</u> 0		0.20	0.20<Φ <u>&lt;</u>	<u>&lt;</u> 0.25	Φ>0.25	
		W - 1	ACC. NO.	3E <i>A</i> 100m	A/ nm²	2		1		0
Minor	Segment Defect	, w <sub>-1</sub> W <sub>1</sub>		W is more than 1/2 so $\Phi = \frac{L + W}{2}$ Distance between two		segment width Reje			Reject	
	Bolost		Note			o defect is 10mm				
			Φ	Φ <u>&lt;</u> 0	.10	0.10<⊕ <u>&lt;</u> 0.20		<u>&lt;</u> 0.25	Ф>0.25	
Minor	Protuberant Segment	w d	W	Glu		W <u>&lt;</u> 1/2 : W <u>&lt;</u> 0.		W <u>&lt;</u> 1/2 W <u>&lt;</u> 0		Ignore
		Φ = ( L + W ) / 2	ACC. NO.	3E/ 100m	A / nm²	2		1		0
			1. Seg	ment		ı				
			E	3	B <u>&lt;</u>	<u>0.4mm</u>	0.4 <e< td=""><td>3<u>&lt;</u>1.0mm</td><td>B&gt;′</td><td>1.0mm</td></e<>	3 <u>&lt;</u> 1.0mm	B>′	1.0mm
	Assembly		B-	A	В-	A<1/2B	В-	A<0.2	B-A	·<0.25
Minor	Mis-alignment	L <sub>B</sub> T - L <sub>A</sub>	Juc	_		ceptable	Acc	eptable	Acc	eptable
			2. Dot	Matrix	[					
			Defo	ormatio	on>2					Reject
Minor	Stain on LCD Panel Surface		Accept when stains can be wiped lightly with a soft cloor a similar one. Otherwise, judged according to the above items: "Black spot" and "White Spot"							

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#### 11. RELIABILITY

NO.	Item	Condition	Criterion
1	High Temperature Operating	50°C, 240Hrs	No defect in cosmetic and operational function allowable.  Total current Consumption should be below double of initial value.
2	Low Temperature Operating	0°C, 240Hrs	
3	High Humidity	40℃, 90%RH, 96Hrs	
4	High Temperature Storage	70℃, 240Hrs	
5	Low Temperature Storage	-20℃, 240Hrs	
6	Vibration	Random wave	
		10 ~ 100Hz	
		Acceleration: 2g	
		2 Hrs per direction(X,Y,Z)	
7	Thermal Shock	0°C to 25°C to 50°C	
		(60Min) (5Min) (60Min)	
		10Cycles	
8	ESD Testing	Contract Discharge Voltage: +1 ~ 5kV and –1 ~ –5kV	There will be discharged ten times at every discharging voltage cycle. The voltage gap is 1kV.
		Air Discharge Voltage: +1 ~ 8kV and –1 ~ -8kV	

Note:

- 1) Above conditions are suitable for GOLDENTEK standard products.
- 2) For restrict products, the test conditions listed as above must be revised.

#### 12. HANDLING PRECAUTIONS

#### (1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

#### (2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifloroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Ketone
- Aromatics

#### (3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

#### (4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

#### (5) Caution for operation

 It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.

#### 12. HANDLING PRECAUTIONS (Continued)

- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them.

However those phenomena do not mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature range.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40°C, 50%RH or less is required.

#### (6) Storage

In the case of storing for a long period of time (for instance ,for years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

#### (7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol.

Which should be burned up later.

- When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

